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Page 1 of 10 APPLICATION NO. ATTY, DOCKET NO. **FORM PTO-1449** 1875.0560002 09/844,296 **INVENTORS** SECOND SUPPLEMENTAL BUCHWALD et al. INFORMATION DISCLOSURE STATEMENT **ART UNIT** FILING DATE 2634 April 30, 2001 **U.S. PATENT DOCUMENTS** EXAMINER DOCUMENT NUMBER NAME CLASS SUB-CLASS | FILING DATE INITIAL DATE 03/1995 Dukes et al. AA1 5.396.224 4 08/1996 Noneman et al. AB1 5,550,546 JP 06/1998 Kline et al. AC1 5.768.268 JP. 10/1998 Cloke et al. **→** AD1 5,822,143 03/1999 Termerinac et al. AE1 5,881,107 JP FOREIGN PATENT DOCUMENTS **EXAMINER** TRANSLATION CLASS SUB-CLASS COUNTRY DATE INITIAL DOCUMENT NUMBER 06/2000 EP N/A AF1 EP 1 006 697 SP N/A 10/2001 EP AG1 EP 1 139 619 W N/A wo 04/2001 WO 01/29991 AH1 2 B OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.) Black, Jr., W. and Hodges, D., "Time Interleaved Converter Arrays," IEEE Journal of Solid-State AI1 Circuits, IEEE, Vol. SC-15, No. 6, December 1980, pages 1022-1029. JP. Conroy, C. et al., "An 8-b 85-MS/s Parallel Pipeline A/D Converter in 1-µm CMOS," IEEE Journal of JP AJ1 Solid-State Circuits, IEEE, Vol. 28, No. 4, April 1993, pages 447-454. Dally, W. and Poulton, J., "Transmitter Equalization for 4Gb/s Signalling," Proceedings of Hot P AK1 Interconnects IV, Palo Alto, CA, 1996, 10 pages. Ellersick, W. et al., "A Serial-Link Transceiver Based on 8GSample/s A/D and D/A Converters in 0.25µm CMOS," IEEE International Solid-State Circuits Conference, IEEE, 2001, page 58-59 and JP AL1 430. Ellersick, W. et al., "GAD: A 12-GS/s CMOS 4-bit A/D Converter for an Equalized Multi-Level Link," JP AM1 Symposium on VLSI Circuits Digest of Technical Papers, 1999, pages 49-52. Eklund, J-E. and Gustafsson, F., "Digital Offset Compensation of Time-Interleaved ADC Using JP Random Chopper Sampling," IEEE International Symposium on Circuits and Systems, IEEE, May AN1 28-31, 2000, pages III-447 thru III-450. DATE CONSIDERED **EXAMINER** 10/19/05 EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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	AF10						No Yes
	AG10						No Yes
	AH10			Author, Title, Date, Pert	Incut Bases	ota \	No
P P	AJ10 AJ10 AK10	Progress Report on E Equalization, January	qualizations	ffects of Transmission Imp Magazine, IEEE, June 199 of Multimode Fibers, IEEE 5 pages.	93, pages 68-	76. Hoc Group o	n
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